

<b>Notice of References Cited</b>	Application/Control No. 09/940,517		Applicant(s)/Patent Under Reexamination HOSHINO ET AL.	
	Examiner Christopher R. Magee		Art Unit 2653	Page 1 of 1

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*	U	Wu et al. "ESD Protection of GMR Heads in Manufacturing." 1999. SAE Magnetics (HK) Ltd. EOS/ESD SYMPOSIUM. pages 99-315 to 99-318.
*	V	IBM Technical Disclosure Bulletin. "Magnetoresistive Head with Integrated Varistor for ESD/EOS Protection." April 1, 2000. IBM. Issue Number 432. Page Number 751.
*	W	IBM Technical Disclosure Bulletin. "Magnetoresistive Head with Integrated Varistor Employing Combined Varistor, Antiferromagnetic Seed/Gap and Grounded First Shield." April 1, 2000. IBM. Issue Number 432. Page Number 751.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
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